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1 Architectural synthesis of high-level analogue VHDL-AMS description using netlist extraction from parse trees

Domenech-Asensi, G.; Kazmierski, T.J.; Ruiz-Marin, J.D.; Ruiz-Merino, R.;
 Electronics Letters, Volume: 36, Issue: 20, 28 Sep 2000
 Pages:1680 - 1682

[\[Abstract\]](#) [\[PDF Full-Text \(300 KB\)\]](#) **IEEE JNL**

2 Using XML for representation and visualization of elaborated VHDL-models

Karayannis, T.; Mades, J.; Schneider, T.; Windisch, A.; Ecker, W.;
 VHDL International Users Forum Fall Workshop, 2000. Proceedings, 18-20 Oct 2000
 Pages:83 - 87

[\[Abstract\]](#) [\[PDF Full-Text \(656 KB\)\]](#) **IEEE CNF**

3 VHDL-AMS modeling of self-organizing neural systems

Lopez-Alcantud, J.-A.; Kazmierski, T.;
 Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE International Symposium on, Volume: 3, 28-31 May 2000
 Pages:734 - 737 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(240 KB\)\]](#) **IEEE CNF**

4 Automated high level synthesis of hardware building blocks present ART-based neural networks, from VHDL-AMS descriptions

Lopez, J.A.; Domenech, G.; Ruiz, R.; Kazmierski, T.J.;
 Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium

on , Volume: 4 , 26-29 May 2002
Pages:IV-77 - IV-80 vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(333 KB\)\]](#) **IEEE CNF**

5 Performance analysis of microelectrofluidic systems using hierarchy modeling and simulation

Tianhao Zhang; Feng Cao; Dewey, A.M.; Fair, R.B.; Chakrabarty, K.;
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on , Volume: 48 , Issue: 5 , May 2001
Pages:482 - 491

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Abstract State Machines: VHDL

(see also [Programming Languages](#) or [Hardware](#))

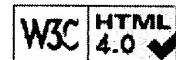
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Jim Huggins / huggins@acm.org

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Relevance scale ☐ ☐ ☐ ☐ ☐**1 SEAMS: simulation environment for VHDL-AMS**
 Peter Frey, Kathiresan Nellayappan, Vasudevan Sahnugasundaram, Ramesh Sankaran
Mayiladuthurai, Chetput L. Chandrashekar, Harold W. Carter
December 1998 **Proceedings of the 30th conference on Winter simulation**

 Full text available: pdf(76.72 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)
2 A formal description of VHDL-AMS analogue systems
 T. Kazmierski
February 1998 **Proceedings of the conference on Design, automation and test in Europe**

 Full text available: pdf(74.50 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)
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A formal definition of the general VHDL-AMS analogue system has been proposed to relate the way in which the language affects the specification of a non-linear discontinuous analogue system. It has been suggested to model the break set as a separate system in order to facilitate the interaction between the analogue equation set and the digital abstract machine. The significance of the proposed model is that it may be used in semantic validation of VHDL-AMS description and may also facilitate mix ...

3 VHDL-AMS: the missing link in system design - experiments with unified modelling in automotive engineering
 E. Moser, N. Mittwollen
February 1998 **Proceedings of the conference on Design, automation and test in Europe**

 Full text available: pdf(72.31 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)
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After the IEEE ballot accepted the first draft language reference manual for VHDL-AMS (IEEE PAR 1076.1) in October 1997, we now can spend time and effort on applying the new arising methodology to real world problems outside the electronic domain. In automotive engineering we have system design problems dealing with hydraulic or mechanic components and their controlling units, for which we expect a major advantage by introducing unified modelling to all domains. With the Brite/EuRam-Project TOOL ...

Keywords: VHDL-AMS, Mixed-Domain Modelling, Mixed-Domain Simulation, Automotive Engineering

4 A component-based approach to modeling and simulating mixed-signal and hybrid systems

Jie Liu, Edward A. Lee

October 2002 **ACM Transactions on Modeling and Computer Simulation (TOMACS)**,
Volume 12 Issue 4

Full text available:  pdf(1.07 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)


Systems with both continuous and discrete behaviors can be modeled using a mixed-signal style or a hybrid systems style. This article presents a component-based modeling and simulation framework that supports both modeling styles. The component framework, based on an actor metamodel, takes a hierarchical approach to manage heterogeneity in modeling complex systems. We describe how ordinary differential equations, discrete event systems, and finite-state machines can be built under this metamodel ...

Keywords: Component-based modeling, Ptolemy II, actors-oriented design, hierarchical heterogeneity, hybrid systems, mixed-signal systems, simulation

5 Behavioral synthesis of field programmable analog array circuits

Haibo Wang, Sarma B. K. Vrudhula

October 2002 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 7 Issue 4

Full text available:  pdf(519.64 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This article presents methods to translate a behavioral-level analog description into a Field Programmable Analog Array (FPAA) implementation. The methods consist of several steps that are referred to as function decomposition, macrocell synthesis, placement and routing, and postplacement simulation. The focus of this article is on the first three steps. The function decomposition step deals with decomposing a high-order system function into a set of lower-order functions. We present an efficient ...

Keywords: Programmable circuits, analog synthesis

6 Mixed-signal design and simulation: Symbolic analysis of analog circuits with hard nonlinearity

Alicia Manthe, Zhao Li, C.-J. Richard Shi

June 2003 **Proceedings of the 40th conference on Design automation**

Full text available:  pdf(220.74 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)


A new methodology is presented to solve a strongly nonlinear circuit, characterized by Piece-Wise Linear (PWL) functions, symbolically and explicitly in terms of its circuit parameters and is amenable to computer implementation. The method is based on a modified nodal formulation of piecewise linear circuit equations as a *mixed* Linear Complementarity Problem (MLCP). The technique of determinant-decision diagrams is applied to implement the symbolic transformation of the MLCP to the standard ...

Keywords: PWL, circuit nonlinearity, symbolic analysis

7 Parallel mixed-technology simulation

Peter Frey, Radharamanan Radhakrishnan

May 2000 **Proceedings of the fourteenth workshop on Parallel and distributed simulation**


Full text available:  pdf(775.49 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Circuit simulation has proven to be one of the most important computer aided design (CAD) methods for the analysis and validation of integrated circuit designs. A popular approach to describing circuits for simulation purposes is to use a hardware description language such as VHDL. Similar efforts have also been carried out in the analog domain that has led to tools such as SPICE. However, with the growing trend of hardware designs that contain both analog and digital components, de ...

8 [From System Specification To Layout: Seamless Top-Down Design Methods for Analog and Mixed-Signal Applications](#)

R. Sommer, I. Rugen-Herzig, E. Hennig, U. Gatti, P. Malcovati, F. Maloberti, K. Einwich, C. Clauss, P. Schwarz, G. Noessing

March 2002 **Proceedings of the conference on Design, automation and test in Europe**

Full text available:  pdf(462.49 KB)

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Design automation for analog/mixed-signal (A/MS) circuits and systems is still lagging behind compared to what has been reached in the digital area. As System-on-Chip (SoC) designs include analog components in more cases, these analog parts become even more a bottle neck in the overall design process. The paper is dedicated to latest R&D activities within the MEDEA+ project ANASTASIA+. Main focus will be the development of seamless top-down design methods for integrated analog and mixed-signal systems ...

9 [A modeling approach to include mechanical microsystem components into the system simulation](#)

R. Neul, U. Becker, G. Lorenz, P. Schwarz, J. Haase, S. Wünsche

February 1998 **Proceedings of the conference on Design, automation and test in Europe**

Full text available:  pdf(117.20 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)



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For MEMS devices modern technologies are used to integrate very complex components and subsystems closely together. Due to mixed-domain problems as well as the occurring interactions between the closely coupled system components the design is a sophisticated process. The interactions between the MEMS components have to be analyzed by system simulation already in an early design stage. In this paper a modeling approach is introduced that enables the incorporation of mechanical microsystem components ...

10 [Behavioral synthesis of analog systems using two-layered design space exploration](#)

Alex Doboli, Adrian Nunez-Aldana, Nagu Dhanwada, Sree Ganesan, Ranga Vemuri

June 1999 **Proceedings of the 36th ACM/IEEE conference on Design automation conference**

Full text available:  pdf(165.35 KB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

11 [Analog synthesis & design methodology: Systematic design of a 200 MS/s 8-bit interpolating/averaging A/D converter](#)

J. Vandebussche, K. Uytendhoeve, E. Lauwers, M. Steyaert, G. Gielen

June 2002 **Proceedings of the 39th conference on Design automation**

Full text available:  pdf(993.97 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The systematic design of a high-speed, high-accuracy Nyquist-rate A/D converter is proposed. The presented design methodology covers the complete flow and is supported by software tools. A generic behavioral model is used to explore the A/D converter's

specifications during high-level design and exploration. The inputs to the flow are the specifications of the A/D converter and the technology process. The result is a generated layout and the corresponding extracted behavioral model. The approach ...

Keywords: A/D converters, flash, interpolating, simulated annealing

12 Modeling methodology: Architectures and languages for model building and reuse: organization and selection of reconfigurable models

Antonio Diaz-Calderon, Christiaan J. J. Paredis, Pradeep K. Khosla

December 2000 **Proceedings of the 32nd conference on Winter simulation**

Full text available:  pdf(314.60 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

This paper introduces the concept of reconfigurable simulation models and describes how these models can be used to support simulation-based design. As in object-oriented programming, a reconfigurable model consists of a separate interface and multiple implementations. An AND-OR tree represents which implementations can be bound to each interface. From the resulting model space, a designer can quickly select the simulation model that is most appropriate for the current design stage. We conclude ...

13 Towards design and validation of mixed-technology SOCs

S. Mir, B. Charlot, G. Nicolescu, P. Coste, F. Parrain, N. Zergainoh, B. Courtois, A. Jerraya, M. Rencz

March 2000 **Proceedings of the 10th Great Lakes Symposium on VLSI**

Full text available:  pdf(581.54 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper illustrates an approach to design and validation of heterogeneous systems. The emphasis is placed on devices which incorporate MEMS parts in either a single mixed-technology (CMOS + micromachining) SOC device, or alternatively as a hybrid system with the MEMS part in a separate chip. The design flow is general, and it is illustrated for the case of applications embedding CMOS sensors. In particular, applications based on finger-print recognition are considered since a ric ...

Keywords: HDLs, MEMS, SOCs, architecture exploration, cosimulation, design, verification

14 Systematic Design of a 200 Ms/S 8-bit Interpolating A/D Converter

J. Vandenbussche, E. Lauwers, K. Uyttenhove, M. Steyaert, G. Gielen

March 2002 **Proceedings of the conference on Design, automation and test in Europe**

Full text available:  pdf(1.34 MB)  Additional Information: [full citation](#), [abstract](#)
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The systematic design of a high-speed, high-accuracyNyquist A/D converter is proposed. The presented designmethodology covers the complete flow and is supported bysoftware tools. A generic behavioral model is used toexplore the A/D converter's specifications during high-leveldesign and exploration. The inputs are thespecifications of the A/D converter and the technologyprocess. The result is a generated layout and thecorresponding extracted behavioral model. The approachhas been applied to a rea ...

15 System synthesis for multiprocessor embedded applications

Luigi Carro, Márcio Kreutz, Flávio R. Wagner, Márcio Oyamada

January 2000 **Proceedings of the conference on Design, automation and test in Europe**

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16 Chemical reaction dynamics: integration of coupled sets of ordinary differential equations on the Caltech hypercube

P. G. Hipes, T. Mattson, Y-S. Wu, A. Kuppermann

January 1989 **Proceedings of the third conference on Hypercube concurrent computers and applications - Volume 2**

Full text available:  pdf(778.88 KB)

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Use of the Caltech/JPL hypercube multicomputer to solve problems in chemical dynamics is the subject of this paper. The specific application is quantum mechanical atom diatomic molecule reactive scattering. One methodology for solving this dynamics problem on a sequential computer is based on symmetrized hyperspherical coordinates. We will discuss our strategy for implementing the hyperspherical coordinate methodology on the hypercube. In particular, the performance of a parallel integrator ...

17 Technology mapping and retargeting for field-programmable analog arrays

Sree Ganesan, Ranga Vemuri

January 2000 **Proceedings of the conference on Design, automation and test in Europe**

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18 Session 5B: Embedded tutorial: CAD solutions and outstanding challenges for mixed-signal and RF IC design: CAD solutions and outstanding challenges for mixed-signal and RFIC design

Domine Leenaerts, Georges Gielen, Rob A. Rutenbar

November 2001 **Proceedings of the 2001 IEEE/ACM international conference on Computer-aided design**

Full text available:  pdf(1.87 MB)

Additional Information: [full citation](#), [abstract](#), [references](#)

This tutorial paper addresses the problems and solutions that are posed by the design of mixed-signal integrated systems on chip (SoC). These include problems in mixed-signal design methodologies and flows, problems in analog design productivity, as well as open problems in analog, mixed-signal and RF design, modeling and verification tools. The tutorial explains the problems that are posed by these mixed-signal/RF SoC designs, describes the solutions and their underlying methods that exist today ...

19 Dynamic NURBS with geometric constraints for interactive sculpting

Demetri Terzopoulos, Hong Qin

April 1994 **ACM Transactions on Graphics (TOG)**, Volume 13 Issue 2

Full text available:  pdf(8.87 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

This article develops a dynamic generalization of the nonuniform rational B-spline (NURBS) model. NURBS have become a defacto standard in commercial modeling systems because of their power to represent free-form shapes as well as common analytic shapes. To date, however, they have been viewed as purely geometric primitives that require the user to manually adjust multiple control points and associated weights in order to design shapes. Dynamic NURBS, or D-NURBS, are physics-based models that ...


Keywords: CAGD, NURBS, constraints, cross-sectional shape design, deformable models, dynamics, finite elements, free-form deformation, optimal curve and surface fitting, shape metamorphosis, solid rounding, trimming

20 Incremental dynamic semantics for language-based programming environments

G. E. Kaiser

April 1989 **ACM Transactions on Programming Languages and Systems (TOPLAS)**,

Volume 11 Issue 2

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Attribute grammars are a formal notation for expressing the static semantics of programming languages—those properties that can be derived from inspection of the program text. Attribute grammars have become popular as a mechanism for generating language-based programming environments that incrementally perform symbol resolution, type checking, code generation, and derivation of other static semantic properties as the program is modified. However, attribute grammars are not suitable for ...

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1 [A formal description of VHDL-AMS analogue systems](#)

T. Kazmierski

 February 1998 **Proceedings of the conference on Design, automation and test in Europe**

Full text available:

 pdf(74.50 KB) [Publisher Site](#)

 Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

A formal definition of the general VHDL-AMS analogue system has been proposed to relate the way in which the language affects the specification of a non-linear discontinuous analogue system. It has been suggested to model the break set as a separate system in order to facilitate the interaction between the analogue equation set and the digital abstract machine. The significance of the proposed model is that it may be used in semantic validation of VHDL-AMS description and may also facilitate mix ...

2 [SEAMS: simulation environment for VHDL-AMS](#)

Peter Frey, Kathiresan Nellayappan, Vasudevan Sahnugasundaram, Ramesh Sankaran Mayiladuthurai, Chetput L. Chandrashekar, Harold W. Carter

 December 1998 **Proceedings of the 30th conference on Winter simulation**

Full text available:

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 Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

3 [Parallel mixed-technology simulation](#)

Peter Frey, Radharamanan Radhakrishnan

 May 2000 **Proceedings of the fourteenth workshop on Parallel and distributed simulation**

Full text available:

pdf(775.49 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Circuit simulation has proven to be one of the most important computer aided design (CAD) methods for the analysis and validation of integrated circuit designs. A popular approach to describing circuits for simulation purposes is to use a hardware description language such as VHDL. Similar efforts have also been carried out in the analog domain that has led to tools such as SPICE. However, with the growing trend of hardware designs that contain both analog and digital components, de ...

4

[Behavioral synthesis of analog systems using two-layered design space exploration](#)

Alex Doboli, Adrian Nunez-Aldana, Nagu Dhanwada, Sree Ganesan, Ranga Vemuri
 June 1999 **Proceedings of the 36th ACM/IEEE conference on Design automation conference**

Full text available:  pdf(165.35 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

5 Behavioral synthesis of field programmable analog array circuits

Haibo Wang, Sarma B. K. Vrudhula

October 2002 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 7 Issue 4

Full text available:  pdf(519.64 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This article presents methods to translate a behavioral-level analog description into a Field Programmable Analog Array (FPAA) implementation. The methods consist of several steps that are referred to as function decomposition, macrocell synthesis, placement and routing, and postplacement simulation. The focus of this article is on the first three steps. The function decomposition step deals with decomposing a high-order system function into a set of lower-order functions. We present an efficient ...

Keywords: Programmable circuits, analog synthesis

6 A component-based approach to modeling and simulating mixed-signal and hybrid systems

Jie Liu, Edward A. Lee

October 2002 **ACM Transactions on Modeling and Computer Simulation (TOMACS)**, Volume 12 Issue 4

Full text available:  pdf(1.07 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Systems with both continuous and discrete behaviors can be modeled using a mixed-signal style or a hybrid systems style. This article presents a component-based modeling and simulation framework that supports both modeling styles. The component framework, based on an actor metamodel, takes a hierarchical approach to manage heterogeneity in modeling complex systems. We describe how ordinary differential equations, discrete event systems, and finite-state machines can be built under this metamodel ...

Keywords: Component-based modeling, Ptolemy II, actors-oriented design, hierarchical heterogeneity, hybrid systems, mixed-signal systems, simulation

7 From System Specification To Layout: Seamless Top-Down Design Methods for Analog and Mixed-Signal Applications

R. Sommer, I. Rugen-Herzig, E. Hennig, U. Gatti, P. Malcovati, F. Maloberti, K. Einwich, C. Clauss, P. Schwarz, G. Noessing

March 2002 **Proceedings of the conference on Design, automation and test in Europe**

Full text available:  pdf(462.49 KB)

Additional Information: [full citation](#), [abstract](#)


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Design automation for analog/mixed-signal (A/MS) circuits and systems is still lagging behind compared to what has been reached in the digital area. As System-on-Chip (SoC) designs include analog components in more cases, these analog parts become even more a bottle neck in the overall design process. The paper is dedicated to latest R&D activities within the MEDEA+ project ANASTASIA+. Main focus will be the development of seamless top-down design methods for integrated analog and mixed-signal systems ...

Modeling methodology: Architectures and languages for model building and reuse: organization and selection of reconfigurable models

Antonio Diaz-Calderon, Christiaan J. J. Paredis, Pradeep K. Khosla

December 2000 **Proceedings of the 32nd conference on Winter simulation**

Full text available:  pdf(314.60 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

This paper introduces the concept of reconfigurable simulation models and describes how these models can be used to support simulation-based design. As in object-oriented programming, a reconfigurable model consists of a separate interface and multiple implementations. An AND-OR tree represents which implementations can be bound to each interface. From the resulting model space, a designer can quickly select the simulation model that is most appropriate for the current design stage. We conclude ...

9 System synthesis for multiprocessor embedded applications

Luigi Carro, Márcio Kreutz, Flávio R. Wagner, Márcio Oyamada

January 2000 **Proceedings of the conference on Design, automation and test in Europe**

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10 Session 5B: Embedded tutorial: CAD solutions and outstanding challenges for mixed-signal and RF IC design: CAD solutions and outstanding challenges for mixed-signal and RFIC design

Domine Leenaerts, Georges Gielen, Rob A. Rutenbar

November 2001 **Proceedings of the 2001 IEEE/ACM international conference on Computer-aided design**

Full text available:  pdf(1.87 MB) Additional Information: [full citation](#), [abstract](#), [references](#)

This tutorial paper addresses the problems and solutions that are posed by the design of mixed-signal integrated systems on chip (SoC). These include problems in mixed-signal design methodologies and flows, problems in analog design productivity, as well as open problems in analog, mixed-signal and RF design, modeling and verification tools. The tutorial explains the problems that are posed by these mixed-signal/RF SoC designs, describes the solutions and their underlying methods that exist today ...

11 VHDL-AMS: the missing link in system design - experiments with unified modelling in automotive engineering

E. Moser, N. Mittwollen

February 1998 **Proceedings of the conference on Design, automation and test in Europe**

Full text available:  pdf(72.31 KB)  Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)
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After the IEEE ballot accepted the first draft language reference manual for VHDL-AMS (IEEE PAR 1076.1) in October 1997, we now can spend time and effort on applying the new arising methodology to real world problems outside the electronic domain. In automotive engineering we have system design problems dealing with hydraulic or mechanic components and their controlling units, for which we expect a major advantage by introducing unified modelling to all domains. With the Brite/EuRam-Project TOOL ...

Keywords: VHDL-AMS, Mixed-Domain Modelling, Mixed-Domain Simulation, Automotive Engineering

12 A modeling approach to include mechanical microsystem components into the system

simulation

R. Neul, U. Becker, G. Lorenz, P. Schwarz, J. Haase, S. Wünsche

February 1998 **Proceedings of the conference on Design, automation and test in Europe**Full text available:  pdf(117.20 KB)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#) [Publisher Site](#)

For MEMS devices modern technologies are used to integrate very complex components and subsystems closely together. Due to mixed-domain problems as well as the occurring interactions between the closely coupled system components the design is a sophisticated process. The interactions between the MEMS components have to be analyzed by system simulation already in an early design stage. In this paper a modeling approach is introduced that enables the incorporation of mechanical microsystem compone ...

13 Analog synthesis & design methodology: Systematic design of a 200 MS/s 8-bit interpolating/averaging A/D converter

J. Vandebussche, K. Uyttenhove, E. Lauwers, M. Steyaert, G. Gielen

June 2002 **Proceedings of the 39th conference on Design automation**Full text available:  pdf(993.97 KB)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The systematic design of a high-speed, high-accuracy Nyquist-rate A/D converter is proposed. The presented design methodology covers the complete flow and is supported by software tools. A generic behavioral model is used to explore the A/D converter's specifications during high-level design and exploration. The inputs to the flow are the specifications of the A/D converter and the technology process. The result is a generated layout and the corresponding extracted behavioral model. The approach ...

Keywords: A/D converters, flash, interpolating, simulated annealing

14 Systematic Design of a 200 Ms/S 8-bit Interpolating A/D Converter

J. Vandebussche, E. Lauwers, K. Uyttenhove, M. Steyaert, G. Gielen

March 2002 **Proceedings of the conference on Design, automation and test in Europe**Full text available:  pdf(1.34 MB) Additional Information: [full citation](#), [abstract](#)[Publisher Site](#)

The systematic design of a high-speed, high-accuracy Nyquist A/D converter is proposed. The presented design methodology covers the complete flow and is supported by software tools. A generic behavioral model is used to explore the A/D converter's specifications during high-level design and exploration. The inputs are the specifications of the A/D converter and the technology process. The result is a generated layout and the corresponding extracted behavioral model. The approach has been applied to a rea ...

15 Mixed-signal design and simulation: Symbolic analysis of analog circuits with hard nonlinearity

Alicia Manthe, Zhao Li, C.-J. Richard Shi

June 2003 **Proceedings of the 40th conference on Design automation**Full text available:  pdf(220.74 KB)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

A new methodology is presented to solve a strongly nonlinear circuit, characterized by Piece-Wise Linear (PWL) functions, symbolically and explicitly in terms of its circuit parameters and is amenable to computer implementation. The method is based on a modified nodal formulation of piecewise linear circuit equations as a *mixed* Linear Complementarity Problem (MLCP). The technique of determinant-decision diagrams is applied to implement the symbolic transformation of the MLCP to the standa ...

Keywords: PWL, circuit nonlinearity, symbolic analysis

16 Towards design and validation of mixed-technology SOCs

S. Mir, B. Charlot, G. Nicolescu, P. Coste, F. Parrain, N. Zergainoh, B. Courtois, A. Jerraya, M. Rencz

March 2000 **Proceedings of the 10th Great Lakes Symposium on VLSI**

Full text available:  pdf(581.54 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper illustrates an approach to design and validation of heterogeneous systems. The emphasis is placed on devices which incorporate MEMS parts in either a single mixed-technology (CMOS + micromachining) SOC device, or alternatively as a hybrid system with the MEMS part in a separate chip. The design flow is general, and it is illustrated for the case of applications embedding CMOS sensors. In particular, applications based on finger-print recognition are considered since a ric ...

Keywords: HDLs, MEMS, SOCs, architecture exploration, cosimulation, design, verification


17 Technology mapping and retargeting for field-programmable analog arrays

Sree Ganesan, Ranga Vemuri

January 2000 **Proceedings of the conference on Design, automation and test in Europe**

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